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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 7

Complete if Known						
Application / Conf. No.	10/086,129 / 2232					
Filing Date	February 28, 2002					
First Named Inventor	David P. Schultz					
Art Unit	2133					
Examiner Name	Unknown					
Attorney Docket Number	X-1069 US					

				U.S. PATENT	DOCUMENTS	
Examiner In: als.*	Cite ∠No.'	Document N Number - Kind (Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner	Cite	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages			
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		EP 0 905 906 A2	03-31-99	Lucent				
		EP 1 235 351 A1	08-28-02	Matsushita Electric	:			
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¹ Applicant's unique cilation designation number (optional).² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ⁵ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁵ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁵ Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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collection of information unless it contains a valid OMB control number Under the Paperwork Reduction Act of 1995, no persons are required to respond to a Complete if Known 10/086,129 2232 Application / Conf. No. February 28, 2 Filing Date **First Named Inventor** David P. Schultz Art Unit 2133 JAN 2 2 2 **1**04 **Examiner Name** Unknown Attorney Docket Number X-1069 US

OTHER -- NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. Examiner Cite No Initials SAYFE KIAEI et al., "VLSI DESIGN OF DYNAMICALLY RECONFIGURABLE ARRAY PROCESSOR-DRAP," IEEE, February 1989, pp. 2484-2488, V3.6, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 VASON P. SRINI, "FIELD PROGRAMMABLE GATE ARRAY (FPGA) IMPLEMENTATION OF DIGITAL SYSTEMS: AN ALTERNATIVE TO ASIC," IEEE, May 1991, pp. 309-314, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 G. MAKI et al., "A RECONFIGURABLE DATA PATH PROCESSOR," IEEE, August 1991, pp. 18-4.1 to 18-4.4, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 <u>IACOB DAVIDSON</u>, "FPGA IMPLEMENTATION OF RECONFIGURABLE MICROPROCESSOR," IEEE, March 1993, pp. 3.2.1 - 3.2.4, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 CHRISTIAN ISELI et al., "BEYOND SUPERSCALER USING FPGA's," IEEE, April 1993, pp. 486-490, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 P.C. FRENCH et al., "A SELF-RECONFIGURING PROCESSOR,"; IEEE, July 1993, pp. 50-59, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 Christian Iseli et al., "SPYDER: A RECONFIGURABLE VLIW PROCESSOR USING FPGA's," IEEE, July 1993, pp. 17-24, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 MICHAEL I. WIRTHLIN et al., "THE NANO PROCESSOR: A LOW RESOURCE RECONFIGURABLE PROCESSOR," IEEE, February 1994, pp. 23-30, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 WILLIAM S. CARTER, "THE FUTURE OF PROGRAMMABLE LOGIC and ITS IMPACT ON DIGITAL SYSTEM DESIGN," April 1994, IEEE, pp. 10-16, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 ANDRE' DEHON, "DPGA-COUPLED MICROPROCESSORS: COMMODITY ICs FOR THE EARLY 21ST CENTURY,"IEEE, February 1994, pp. 31 - 39, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 OSAMA T. ALBAHARNA, "AREA & TIME LIMITATIONS OF FPGA-BASED VIRTUAL HARDWARE," IEEE, April 1994, pp. 184 - 189, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997 Examiner Date

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Considered

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Attorney Docket Number	X-1069 US					

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of Sheet

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Application / Conf. No.	10/086,129 / 2232
Filing Date	February 28, 2002
First Named Inventor	David P. Schultz
Art Unit	2133
Examiner Name	Unknown IAN 2 2 2004
Attorney Docket Number	X-1069 US

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Signature Considered *EXAMINER: Initial if reference considered, whether of not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Sheet 5 of 7

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Application / Conf. No.	10/086,129 / 2232				
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First Named Inventor	David P. Schultz				
Art Unit	2133				
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Initials *		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY	Applicant of Cited Document	or Relevant Figures Appear	T 6	
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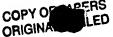
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INFORMATION DISCLOSUBER STATEMENT BY APPLICANT

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Sheet

Complete if Known					
Application / Conf. No.	10/086,129 / 2232				
Filing Date	February 28, 2002				
First Named Inventor	David P. Schultz				
Art Unit	2133				
Examiner Name	Unknown				
Attorney Docket Number	X-1069 US				

			U.S. PATENT	DOCUMENTS	
Examiner In fals	Cite No.'	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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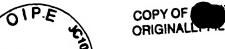
	FOREIGN PATENT DOCUMENTS							
Examiner Initials *	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Finures Appear	т 6		
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Substitute for form 1449A/PTO 2232 Application / Conf. No. 10/086,129 INFORMATION DISCLOSURE Filing Date February 28, 2002 David P. Schultz **First Named Inventor** STATEMENT BY APPLICANT Art Unit 2133 (use as many sheets as necessary) **Examiner Name** Unknown **Attorney Docket Number** X-1069 US Sheet 2 of

OTHER NON PATENT LITERATURE DOCUMENTS							
Examiner Initials *	Cite No ⁻¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²				
	/	Cary D. Snyder and Max Baron; "Xilinx's A-to-Z System Platform"; Cahners Microprocessor; The Insider's Guide to Microprocessor Hardware; Microdesign Resources; February 6, 2001; pp. 1-5.					
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